



KARNATAK LAW SOCIETY'S
 VISHWANATHRAO DESHPANDE RURAL INSTITUTE OF TECHNOLOGY, Haliyal - 581 329
 Department of Electronics & Communication Engineering

All theory classes are of 60 minutes and lab sessions are of 3 hours duration.

Class: 6B

**BLOCK: 9
 29/01/2018**

With Effect from:

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	CCN	DC	ARM	VLSI		B1/B2 : EC LAB/CN LAB		
TUESDAY	B2/B3 : EC LAB/CN LAB				<u>VLSI</u>	<u>ARM</u>	<u>CCN</u>	
WEDNESDAY	VLSI	CMC/DSS/ μ E	PAP	CCN		B3/B1 : EC LAB/CN LAB		
THURSDAY	ARM	CCN	DC	VLSI		<u>CMC/DSS/μE</u>	<u>PAP</u>	
FRIDAY	CMC/DSS/ μ E	DC	CCN	PAP		<u>TUTORIAL</u>	<u>TUTORIAL</u>	
SATURDAY	PAP	ARM	CMC/DSS/ μ E	DC				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15EC61	Digital Communication (DC)	Prof. A. S. Joshi	15EC654	Digital Switching Systems (DSS)	Prof. Kalmesh. W
15EC62	ARM Microcontroller & Embedded Systems (ARM)	Prof. Arpitha. H	15EC655	Microelectronics (μ E)	Prof. Vikas. B
15EC63	VLSI Design (VLSI)	Prof. Meenaxi. T	15CS664	Python Application Programming (PAP)	Prof. Raveendra. M
15EC64	Computer Communication Networks (CCN)	Prof. Kanchan	15ECL67	Embedded Controller Lab (EC LAB)	Prof. Arpitha. H

H.O.D

Principal



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15EC651	Cellular Mobile Communication (CMC)	Prof. Suma Huddar	15ECL68	Computer Networks Lab (CN LAB)	Prof. Kanchan
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