



KARNATAK LAW SOCIETY'S
VISHWANATHRAO DESHPANDE RURAL INSTITUTE OF TECHNOLOGY, Haliyal - 581 329
Department of Electronics & Communication Engineering

Class: 4A

BLOCK: 7

With Effect from: 29/01/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	μ P	LIC	MATHS	S&S		A1/A2 : μ P LAB/LIC LAB		
TUESDAY	A2/A3 : μ P LAB/LIC LAB				<u>CS</u>	<u>S&S</u>	<u>MATHS</u>	
WEDNESDAY	MATHS	CS	μ P	LIC		<u>PCS</u>	<u>S&S</u>	
THURSDAY	PCS	CS	LIC	μ P		A3/A1 : μ P LAB/LIC LAB		
FRIDAY	S&S	PCS	μ P	MATHS		<u>TUTORIAL</u>	<u>TUTORIAL</u>	
SATURDAY	LIC	S&S	PCS	CS				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15MAT41	Engg. Mathematics – IV (MATHS)	Prof. Meenal. K	15EC45	Principles of Communication Systems (PCS)	Prof. Basavaraj. D
15EC42	Microprocessor (μ P)	Prof. Prasann. K	15EC46	Linear Integrated Circuits (LIC)	Prof. Vinay. C
15EC43	Control Systems (CS)	Prof. Rahul. C. M	15ECL47	Microprocessor Lab (μ P LAB)	Prof. Prasann. K
15EC44	Signals and Systems (S&S)	Prof. SURAJ KADLI	15ECL48	Linear IC's and communication Lab (LIC LAB)	Prof. Vinay. C

All theory classes are of 60 minutes and lab sessions are of 3 hours duration.

H.O.D

Principal