



KARNATAK LAW SOCIETY'S
 VISHWANATHRAO DESHAPANDE RURAL INSTITUTE OF TECHNOLOGY, Haliyal - 581 329
 Department of Electronics & Communication Engineering

Class: 6A

BLOCK: 8
29/01/2018

With Effect from:

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	A1/A2 : EC LAB/CN LAB				<u>VLSI</u>	<u>CCN</u>	<u>PAP</u>	
TUESDAY	DC	VLSI	CCN	PAP		A2/A3 : EC LAB/CN LAB		
WEDNESDAY	PAP	CMC/DSS/ μ E	ARM	VLSI		<u>DC</u>	<u>CCN</u>	
THURSDAY	A3/A1 : EC LAB/CN LAB				<u>ARM</u>	<u>CMC/DSS/μE</u>	<u>DC</u>	
FRIDAY	CMC/DSS/ μ E	DC	VLSI	ARM		<u>TUTORIAL</u>	<u>TUTORIAL</u>	
SATURDAY	ARM	CCN	CMC/DSS/ μ E	PAP				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15EC61	Digital Communication (DC)	Prof. Raghavendra. N	15EC654	Digital Switching Systems (DSS)	Prof. Kalmesh. W
15EC62	ARM Microcontroller & Embedded Systems (ARM)	Prof. Jyothi. B. R	15EC655	Microelectronics (μ E)	Prof. Vikas. B
15EC63	VLSI Design (VLSI)	Prof. Vikas. B	15CS664	Python Application Programming (PAP)	Prof. Raveendra. M
15EC64	Computer Communication Networks (CCN)	Prof. A. V. Kolaki	15ECL67	Embedded Controller Lab (EC LAB)	Prof. Jyothi. B. R
15EC651	Cellular Mobile Communication (CMC)	Prof. Suma Huddar	15ECL68	Computer Networks Lab (CN LAB)	Prof. A. V. Kolaki

H.O.D

Principal