



KARNATAK LAW SOCIETY'S
VISHWANATHRAO DESHPANDE RURAL INSTITUTE OF TECHNOLOGY, Haliyal - 581 329
Department of Electronics & Communication Engineering

Class: 6B PARALLEL BATCH

**BLOCK: 7, 9 & PG CLASS
29/01/2018**

With Effect from:

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	μP (7)	DC (9)	OS					
TUESDAY			AMD	A&P	<u>OS</u>			
WEDNESDAY	A&P	μEC	μP (7)	AMD		μP LAB		
THURSDAY	AMD	A&P	DC (9)	μP (7)		<u>μEC</u>	<u>OS</u>	
FRIDAY	μEC	DC (9)	μP (7)	AMD		Ad Comm LAB		
SATURDAY	OS	A&P	μEC	DC (9)				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
10EC61	Digital Communication (DC)	Prof. A. S. Joshi	10EC65	Operating Systems (OS)	Prof. Kanchan
10EC62	Microprocessors (μP)	Prof. Prasann. K	10EC661	Analog and Mixed Mode VLSI Design (AMD)	Prof. V. M. Chougala
10EC63	Microelectronics Circuits (μEC)	Prof. Vikas. B	10ECL67	Advanced Communication Lab (Ad Comm LAB)	Prof. Raghavendra. N
10EC64	Antennas and Propagation (A&P)	Prof. Plasin. D	10ECL68	Microprocessor Lab (μP LAB)	Prof. Komal. P

All theory classes are of 60 minutes and lab sessions are of 3 hours duration.

H.O.D

Principal