



Department of Electronics & Communication Engineering

Class: 5A

BLOCK: 8

With Effect from: 14/08/2017

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	OS (8&9)/SAFAT (7)	MGMT	AE (8)/C++ (9)	ITC		A1/A2 : DSP LAB/HDL LAB		
TUESDAY	MGMT	DSP	HDL	HDL (Tutorial)		A2/A3 : DSP LAB/HDL LAB		
WEDNESDAY	AE (8)/C++ (9)	ITC	OS (8&9)/SAFAT (DE LAB)	DSP		<u>MGMT</u>	<u>HDL</u>	
THURSDAY	A3/A1 : DSP LAB/HDL LAB				<u>ITC</u>	<u>DSP</u>	<u>ITC (Tutorial)</u>	
FRIDAY	DSP	ITC	MGMT	HDL		<u>AE (8)/C++ (9)</u>	<u>OS (8&9)/SAFAT (6)</u>	
SATURDAY	HDL	AE (8)/C++ (9)	DSP	OS (8&9)/SAFAT (DE LAB)				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15ES51	Management & Entrepreneurship (MGMT)	Prof. Suma. H	15EC553	Operating Systems (OS)	Prof. Placin. D
15EC52	Digital Signal Processing (DSP)	Prof. Bhagyashree. R	15EC561	Automotive Electronics (AE)	Prof. Rohini. K
15EC53	Verilog HDL (HDL)	Prof. Meenaxi. T	15EC562	Object Oriented Programming Using C++ (C++)	Prof. Basavaraj. D
15EC54	Information Theory and Coding (ITC)	Prof. Prasann. K	15ECL57	DSP Laboratory	Prof. Bhagyashree. R
15EC552	Switching & Finite Automata Theory (SFAT)	Prof. Raveendra. M	10ECL58	HDL Laboratory	Prof. Meenaxi. T

All theory classes are of 60 minutes and lab sessions are of 3 hours duration.

H.O.D

Principal