



KARNATAK LAW SOCIETY's
VISHWANATHRAO DESHPANDE RURAL INSTITUTE OF TECHNOLOGY, Haliyal - 581 329
Department of Electronics & Communication Engineering

Class: M.Tech 4th semester

Block: LD LAB

w.e.f: 27/01/2017

Date / Time	9 am	10 am	11.10 am	12.10 pm	1.15 pm	1.45 pm / 2.15 pm	3.15 pm	4.15 pm
MONDAY	Project Work							
TUESDAY	Seminar							
WEDNESDAY	RF & MW		PCI					
THURSDAY	PCI		RF & MW					
FRIDAY	Project Work							
SATURDAY								

Sl. No	Subject Code	Subject Title	Staff
1.	14EIE41	RF & microwave circuit design	Prof. Basavaraj D.
2.	14EIE422	Process Control Instrumentation	Prof. A. S. Joshi.


Head
HEAD

Dept. of Electronic & Communication Engg
KLS V.D.R. Institute of Technology
HALIYAL-581 329


Principal
Principal

CL's Vishwanathrao Deshpande
Rural Institute of Technology
Haliyal.