



KARNATAK LAW SOCIETY'S

VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY, Haliyal - 581 329

Department of Electronics & Communication Engineering

Class: 3A

BLOCK: 7

With Effect from: 01/08/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	DE	NA	EI	MATHS		A1/A2 : AE LAB/DE LAB		
TUESDAY	MATHS	EI	DE	EE		A2/A3 : AE LAB/DE LAB		
WEDNESDAY	A3/A1 : AE LAB/DE LAB				<u>AE</u>	CIP	<u>EE</u>	
THURSDAY	AE	MATHS	EE	DE		<u>EI</u>	<u>NA</u>	
FRIDAY	NA	DE	AE	EI		<u>AE</u>	<u>MATHS</u>	
SATURDAY	EE	NA	MATHS	AE				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
17MAT31	Engg Mathematics-III (MATHS)	Prof. Akshata. P	17EC36	Engg Electromagnetics (EE)	Prof. Basavaraj. D
17EC32	Electronic Instrumentation (EI)	Prof. V. M. Chougala	17ECL37	AE Lab	Prof. Deepak Sharma
17EC33	Analog Electronics (AE)	Prof. Deepak Sharma	17ECL38	DE Lab	Prof. Komal Pawar
17EC34	Digital Electronics (DE)	Prof. Komal Pawar			
17EC35	Network Analysis (NA)	Prof. A. S. Joshi			

All theory classes are of 60 minutes and lab sessions are of 3 hours duration.

H.O.D

Principal



KARNATAK LAW SOCIETY'S

VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY, Haliyal - 581 329

Department of Electronics & Communication Engineering

Class: 3B

BLOCK: 10

With Effect from: 01/08/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	DE	MATHS	EI	EE		<u>AE</u>	<u>NA</u>	
TUESDAY	EE	NA	DE	MATHS		<u>EI</u>	<u>AE</u>	
WEDNESDAY	DE	AE	EE	NA		B1/B2 : AE LAB/DE LAB		
THURSDAY	EI	DE	AE	MATHS		B2/B3 : AE LAB/DE LAB		
FRIDAY	B3/B1 : AE LAB/DE LAB				<u>EE</u>	<u>MATHS</u>	<u>CIP</u>	
SATURDAY	MATHS	AE	NA	EI				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
17MAT31	Engg Mathematics-III (MATHS)	Prof. Akshata. T	17EC36	Engg Electromagnetics (EE)	Prof. Basavaraj. D
17EC32	Electronic Instrumentation (EI)	Prof. Kalmesh. M. W	17ECL37	AE Lab	Prof. Rahul. C. M
17EC33	Analog Electronics (AE)	Prof. Rahul. C. M	17ECL38	DE Lab	Prof. Prasann. K
17EC34	Digital Electronics (DE)	Prof. Prasann. K			
17EC35	Network Analysis (NA)	Prof. Anupama. A			

All theory classes are of 60 minutes and lab sessions are of 3 hours duration.

H.O.D

Principal



Class: 5A

BLOCK: 8

With Effect from: 01/08/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	AE(8)/C++(9)	MGMT	HDL	ITC		<u>OS</u>	<u>DSP</u>	
TUESDAY	A1/A2 : DSP LAB/HDL LAB				<u>HDL</u>	<u>MGMT</u>	<u>ITC</u>	
WEDNESDAY	DSP	OS	MGMT	ITC		A2/A1 : DSP LAB/HDL LAB		
THURSDAY	ITC	AE(8)/C++(9)	DSP	MGMT				
FRIDAY	OS	HDL	DSP	AE(8)/C++(9)				
SATURDAY	HDL	DSP	OS	AE(8)/C++(9)				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15ES51	Management & Entrepreneurship (MGMT)	Prof. A. V. Kolaki	15EC561	Automotive Electronics (AE)	Prof. Rohini. K
15EC52	Digital Signal Processing (DSP)	Prof. SURAJ KADLI	15EC562	Object Oriented Programming Using C++ (C++)	Prof. Raveendra. M
15EC53	Verilog HDL (HDL)	Prof. Meenakshi. T	15ECL57	DSP Lab	Prof. SURAJ KADLI
15EC54	Information Theory and Coding (ITC)	Prof. Komal Pawar	15ECL58	HDL Lab	Prof. Meenakshi. T
15EC553	Operating Systems (OS)	Prof. Raghavendra. N			

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KARNATAK LAW SOCIETY'S

VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY, Haliyal - 581 329

Department of Electronics & Communication Engineering

Class: 5B

BLOCK: 9

With Effect from: 01/08/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	AE(8)/C++(9)	OS	DSP	HDL		B1/B2 : DSP LAB/HDL LAB		
TUESDAY	DSP	HDL	OS	MGMT		B2/B3 : DSP LAB/HDL LAB		
WEDNESDAY	B3/B1 : DSP LAB/HDL LAB				<u>MGMT</u>	<u>ITC</u>	<u>DSP</u>	
THURSDAY	HDL	AE(8)/C++(9)	MGMT	OS		<u>DSP</u>	<u>ITC</u>	
FRIDAY	MGMT	ITC	DSP	AE(8)/C++(9)				
SATURDAY	OS	HDL	ITC	AE(8)/C++(9)				

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15ES51	Management & Entrepreneurship (MGMT)	Prof. V. M. Chougala	15EC561	Automotive Electronics (AE)	Prof. Rohini. K
15EC52	Digital Signal Processing (DSP)	Prof. Vinay Chitare	15EC562	Object Oriented Programming Using C++ (C++)	Prof. Raveendra. M
15EC53	Verilog HDL (HDL)	Prof. Nikhil Kulkarni	15ECL57	DSP Lab	Prof. Vinay Chitare
15EC54	Information Theory and Coding (ITC)	Prof. Suma Huddar	15ECL58	HDL Lab	Prof. Nikhil Kulkarni
15EC553	Operating Systems (OS)	Prof. Raghavendra. N			

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VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY, Haliyal - 581 329

Department of Electronics & Communication Engineering

Class: 7A

BLOCK: 6

With Effect from: 06/08/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	AC1/V1 : AC LAB/VLSI LAB				<u>DIP</u>	<u>PE</u>	<u>μW&A</u>	
TUESDAY	DIP	PE	DSPA	μW&A		AC3 : AC LAB		
WEDNESDAY	AC2/V2 : AC LAB/VLSI LAB				<u>DSPA</u>	<u>μW&A</u>	<u>RTS(10)/CRYPTO(6)</u>	
THURSDAY		PE	DIP	DSPA		<u>RTS(10)/CRYPTO(6)</u>	<u>μW&A</u>	
FRIDAY		RTS(10)/CRYPTO(6)	DSPA	PE		<u>DIP</u>	<u>RTS(7)/CRYPTO(6)</u>	
SATURDAY	Project Work Phase-I + Project work Seminar							

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15EC71	Microwave and Antennas (μW&A)	Prof. Placin. D	15EC751	DSP Algorithms and Architecture (DSPA)	Prof. Kanchan
15EC72	Digital Image Processing (DIP)	Prof. Suma Huddar	15ECL76	Advanced Communication Lab	Prof. Raghavendra. N
15EC73	Power Electronics (PE)	Prof. A. S. Joshi	15ECL77	VLSI Lab	Prof. Vikas. B
15EC743	Real Time Systems (RTS)	Prof. Nikhil K	15ECP78	Project Work Phase-I + Project work Seminar	
15EC744	Cryptography (CRYPTO)	Prof. A. V. Kolaki			

All theory classes are of 60 minutes and lab sessions are of 3 hours duration.

H.O.D

Principal



KARNATAK LAW SOCIETY'S

VISHWANATHRAO DESHPANDE INSTITUTE OF TECHNOLOGY, Haliyal - 581 329

Department of Electronics & Communication Engineering

Class: 7B

BLOCK: As indicated

With Effect from: 06/08/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	μ W&A (6)	PE (6)	DSPA (6)	DIP (6)		AC3 : AC LAB		
TUESDAY	AC1/V1 : AC LAB/VLSI LAB				<u>DIP (7)</u>	<u>μW&A (7)</u>	<u>DSPA (7)</u>	
WEDNESDAY		PE (6)	μ W&A (6)	DSPA (6)		<u>DIP (10)</u>	<u>RTS(10)/CRYPTO (6)</u>	
THURSDAY	AC2/V2 : AC LAB/VLSI LAB				<u>PE (10)</u>	<u>RTS(10)/CRYPTO (6)</u>	<u>DIP (10)</u>	
FRIDAY		RTS(10)/CRYPTO (6)	μ W&A (10)	PE (10)		<u>DSPA (7)</u>	<u>RTS(7)/CRYPTO (6)</u>	
SATURDAY	Project Work Phase-I + Project work Seminar							

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
15EC71	Microwave and Antennas (μ W&A)	Prof. Plasin. D	15EC751	DSP Algorithms and Architecture (DSPA)	Prof. SURAJ KADLI
15EC72	Digital Image Processing (DIP)	Prof. Anupama. A	15ECL76	Advanced Communication Lab	Prof. Kalmesh. M. W
15EC73	Power Electronics (PE)	Dr. Arun L. Kakhandki	15ECL77	VLSI Lab	Prof. Vikas. B
15EC743	Real Time Systems (RTS)	Prof. Nikhil K	15ECP78	Project Work Phase-I + Project work Seminar	
15EC744	Cryptography (CRYPTO)	Prof. A. V. Kolaki			

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H.O.D

Principal



Class: 7B PARALLEL BATCH

BLOCK: As indicated

With Effect from: 06/08/2018

DAY	9:00	10:00	11:10	12:10	<u>1:15</u>	1:45/ <u>2:15</u>	<u>3:15</u>	<u>4:15</u>
MONDAY	CCN (DE LAB)	PE (6)	DSPA (6)	ESD (DE LAB)				
TUESDAY		ESD (8)	TUTORIAL (8)	OFC (8)		<u>CCN (10)</u>	<u>DSPA (7)</u>	
WEDNESDAY	OFC (9)	PE (6)	CCN (9)	DSPA (6)		PE LAB		
THURSDAY		CCN (DE LAB)	ESD (DE LAB)		<u>PE (10)</u>	<u>RTS (10)</u>	<u>OFC (8)</u>	
FRIDAY	ESD (AC LAB)	RTS (10)	OFC (AC LAB)	PE (10)		<u>DSPA (7)</u>	<u>RTS (7)</u>	
SATURDAY	VLSI LAB							

CODE	SUBJECT	STAFF	CODE	SUBJECT	STAFF
10EC71	Computer Communication Networks (CCN)	Prof. Kanchan	10EC762	Real Time Systems (RTS)	Prof. Nikhil Kulkarni
10EC72	Optical Fiber Communication (OFC)	Prof. Kalmesh. M. W	10ECL77	VLSI Lab	Prof. Vikas. B
10EC73	Power Electronics (PE)	Dr. Arun L. Kakhandki	10ECL78	Power Electronics Lab	Prof. A. S. Joshi
10EC74	Embedded System Design (ESD)	Prof. Plasin. D			
10EC751	DSP Algorithms & Architecture (DSPA)	Prof. SURAJ KADLI			

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